#### KOTA NIRMALA JYOTHI

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E-MAIL: kotanirmalajyothi@gmail.com

### CAREER OBJECTIVE:

To pursue my career in an organization with a performance oriented environment for achieving personal advancement. Being ambitious and hard working, I am looking forward to utilise my potential and being worthy of managing my trust and confidence in achieving organizational goals.

#### **EDUCATIONAL QUALIFICATION:**

S.NO	Qualification	Institution	Board/university	Passing Year	Marks/ CGPA
1	M.tech (Vlsi and embedded system)	JNTU Kakinada	JNTU Kakinada	2025	pursuing
2	B.Tech (ECE)	Pragati Engineering College	JNTU Kakinada	2023	7.59
3	Intermediate	Narayana junior college	Board of intermediate education	2019	9.21
4	SSC	Durga Prasad school	Board of secondary school education	2017	7.8

## ACADEMIC PROJECT WORK:

- Mini project "LIFI AUDIO TRANSMISSION".
- Major project "DEEP LEARNING FROM NOISY IMAGE LABELS WITH QUALITY EMBEDDING USING MATLAB".
- "An online platform to link potential dairy industries and dairy farmers" -Idea submitted in smart India hackathon 2022.
- Mini project in M.tech "M.O.C.C.C.C.T.A" using finfet.

### ACHIEVEMENTS AND AWARDS:

- Awarded as runner up in central government event Smart India Hackathon 2022 as a team member.
- Participated in EJIVE, a technical event conducted by the department of ECE in 2021 and active coordinator of "EJIVE" event in 2022.
- Participated in the 63rd national school game of karate in 2018 and placed gold medal, and also awarded prathibha award from govt.

#### PERSONAL ASSESSMENT:

- An enthusiastic fresher with highly motivated and leadership skills having bachelors of engineering in vlsi department.
- Eager to new technologies and methodologies.
- Ability to work as well as group.

### WORKSHOPS AND SEMINARS:

- Participated in a one day workshop on "Hands-on training on xilinx".
- Attended seminar "IC FABRICATION UNIT" from ISE bangalore.
- Attended a workshop on "Develop custom embedded board for robotic applications.

### SOFTWARE PROFICIENCY:

- Programming language: python, c language.
- HDL CODING: Basics of verilog.
- Packages: MS OFFICE.
- VLSI Software packages: Xilinx ,cadence,LTspice.

### **CERTIFICATES:**

- Celnois- academic process mining Fundamentals and expert internship.
- Cisco introduction of cyber security and cyber security essentials.

### PERSONAL PROFILE:

Name : K . Nirmala Jyothi Father name: K . David Vara

Prasad Date of birth:

09/01/2002

Nationality: Indian

Languages known: English, Telugu.

Attributes: hardworking ,self-motivated, having good communication skills.

# **DECLARATION:**

I hereby affirm that the information furnished above authentic to the best of my knowledge and belief.

K. Nirmala Jyothi